ABSTRACT OF THE DISCLOSURE

In a liquid crystal display device having a thin film transistor in a pixel region thereof, the present invention arranges a semiconductor layer being located under a source electrode in the thin film transistor within a contour of a gate line (or a gate electrode of the thin film transistor) so as to suppress a photo conductive current in the semiconductor layer, or to prevent the source electrode from being broken at a position where the source electrode gets over the semiconductor layer.